

PRODUCT SPECIFICATION

240*64 Graphic COB LCD MODULE MODEL: LT-24064B-901 Ver:1.0

< \bigcirc > Finally Specification

	CUSTOMER'	S APPROVAL
CUSTOMER :		
SIGI	NATURE:	DATE:

APPROVED	РМ	PD	PREPARED
BY	REVIEWD	REVIEWD	Ву

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1. Features

The features of LCD are as follows

- * Display mode : DFSTN/Transmissive/Negative
- * Drive IC : RA6963C/SDN8080G
- * Interface : 8080 8-Bit MPU Interface
- * Driving Method : 1/64 Duty, 1/9 Bias
- * Viewing Direction : 6 O'clock
- * Backlight : LED Red(Side)
- *Sample NO : EY2406C4WVR6B-1.0/20100626

2. MECHANICAL SPECIFICATIONS

Item	Specification	Unit
Module Size	135.6(W) x 51.2(H) x 10.9MAX(T)	mm
Viewing Area	111(W) x 34 (H)	mm
Activity Area	105.56(W) x28.12(H)	mm
Number of Dots	240 X 64 Dots	-
Dot Size	0.4 (W) x 0.4 (H)	mm
Dot Pitch	0.44(W)x0.44(H)	mm

3. ELECTRICAL SPECIFICATIONS

3-1 ABSOLUTR MAZIMUM RATINGS (Ta = 25 °C)

ltem	Symbol	Standard Value				
item	Symbol	Min.	Тур.	Max.	Unit	
Supply Voltage For Logic	Vdd – Vss	-0.3	-	7.0	V	
Supply Voltage For LCD Drive	V _{LCD}	0	-	32	V	
Input Voltage	Vin	-0.3	-	VDD+0.3	V	
Operating Temp.	Тор	-20	-	+70	°C	
Storage Temp.	Tst	-30	-	+80	°C	

*. NOTE: The response time will be extremely slow when the operating temperature is around -20 $^{\circ}$ C, and the back ground will become darker at high temperature operating.

3-2 ELECTRICAL CHARACTERISTICS

ltem	1	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Logic supply	Voltage	VDD – Vss		4.5	5.0	5.5	V
LCD Drive		V _{LCD}		12.5	12.8	13.1	V
Input Voltage	"H" Level	V _{IH}	Ta = 25 °C	VDD-2.2	-	Vdd	V
	"L" Level	V _{IL}	$V_{DD}=5V\pm10\%$	0	-	0.8	V
Frame Free	quency	f _{FLM}		-	60	-	Hz
Current Cons	sumption	I _{DD}		-	25.4	-	mA

3-3. BACKLIGHT

3-3-1. Absolute Maximum Ratings

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Forward Current	IF		40	50	60	mA
Reverse Voltage	VR	Ta = 25 °C	-	-	4	V
Power Dissipation	PD		-	-	162	mW

3-3-2. Electrical-optical Characteristics

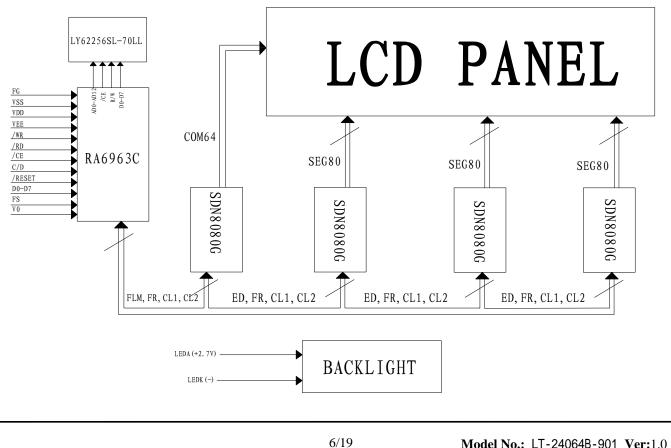
Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Forward Voltage	VF		-	2.7	-	V
Average Luminous Intensity	lv	IF=50mA Ta = 25 °C	140	-	-	cd/m ²
Emission wavelength	v	1a - 25 C	620	625	630	nm

The brightness is measured without LCD panel

4. TERMINAL FUNCTIONS AND BLOCK DIAGRAM **4-1. INTERFACE PIN FUNCTION DESCRIPTION**

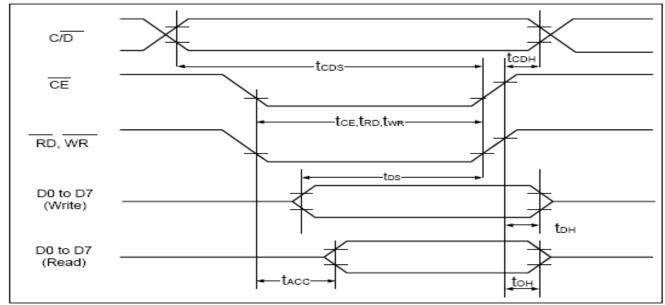
Pin No.	Pin Name	Function
1	FG	Frame Ground
2	VSS	Ground
3	VDD	Supple voltage for logic circuit
4	VEE	Supple voltage for LCD driving
5	/WR	Enable signal select read or write action
6	/RD	Enable signal
7	/CE	Chip enable pin for display momery of any address
8	C/D	A signal for selecting registers
9	NC	No connection
10	/RESET	Reset signal pin
11-18	D0-D7	8-bit data bus
19	FS	Font Selection
20	V0	Pin for negative voltage supple
21	LEDA	Backlight (+5.0V)
22	LEDK	Backlight (-)

4-2. BLOCK DIAGRAM



5.TIMING CHARACTERISTICS

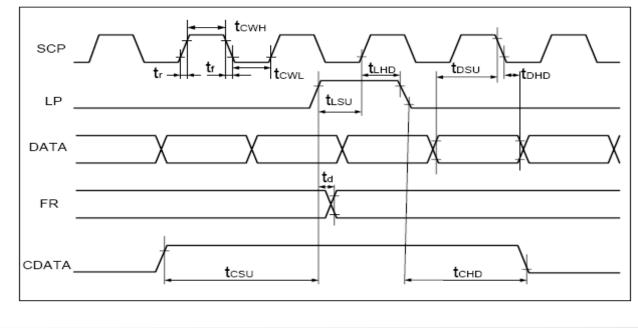
5-1 MPU Interface Timing



($V_{\text{DD}}\text{=+5V}\pm5\%,\text{GND}\text{=}0\text{V},\text{Ta}\text{=-20}\text{ to +70}^\circ\text{C}$)

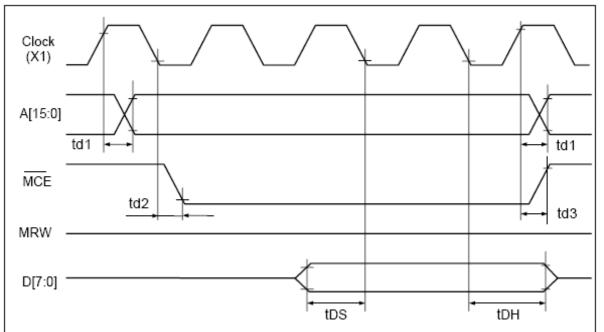
		(.00		-	
ltem	Symbol	Test Conditions	Min.	Max.	Unit
C/ D Set Up Time	t _{CDS}		100		ns
C/ D Hold Time	t _{CDH}		10		ns
\overline{CE} , \overline{RD} , \overline{WR} Pulse Width	t_{CE},t_{RD},t_{WR}		80		ns
Data Set Up Time	t _{DS}		80		ns
Data Hold Time	t _{DH}		40		ns
Access Time	t _{ACC}			150	ns
Output Hold Time	t _{OH}		10	50	ns

5-2 Driver Interface Timing

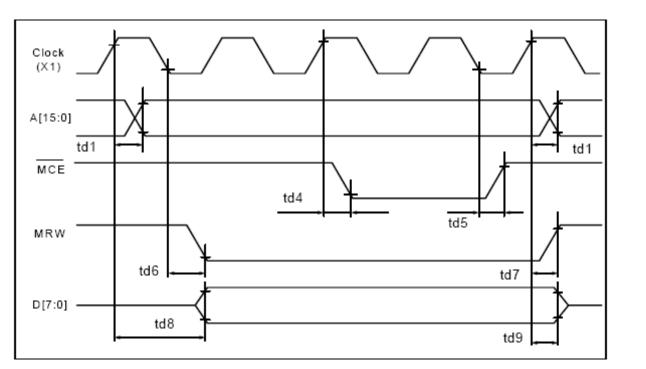


		(V _{DD} =+5V±5%,GND=0V,Ta= -20 to +70℃					
ltem	Symbol	Test Conditions	Min.	Max.	Unit		
Operating Frequency	f _{SCP}	Ta = -20~70°∁		2.75	MHz		
SCP Pulse Width	t _{CWH} , t _{CWL}		150		ns		
SCP Rise/Fall Time	t _r ,t _f			30	ns		
LP Setup Time	t _{LSU}		150	290	ns		
LP Hold Time	t _{LHD}		5	40	ns		
Data Setup Time	t _{DSU}		170		ns		
Data Hold Time	t _{DHD}		80		ns		
FR Delay Time	t _d		0	90	ns		
CDATA Setup Time	t _{CSU}		450	850	ns		
CDATA Hold Time	t _{CHD}		450	950	ns		

5-3 External Memory Interface 5-3-1 External RAM Read



5-3-2 External RAM Write



($V_{\text{DD}}\text{=+5V\pm5\%},\text{GND=0V},\text{Ta=-20 to +70}^\circ\!\!\!\!^\circ\!\!\!^\circ$)

ltem	Symbol	Test Conditions	Min.	Max.	Unit
Address Delay Time	t _{d1}			250	ns
MCE Fall Delay Time(Read)	t _{d2}			180	ns
MCE Rise Delay Time(Read)	t _{d3}			180	ns
Data Setup Time	t _{DS}				ns
Data Hold Time	t _{DH}				ns
MCE Fall Delay Time(Write)	t _{d4}			200	ns
MCE Rise Delay Time(Write)	t _{d5}			200	ns
MRW Fall Delay Time	t _{d6}			180	ns
MRW Rise Delay Time	t _{d7}			180	ns
Data Stable Time	t _{d8}			450	ns
Data Hold Time	t _{d9}			200	ns

6. COMMAND LIST

Command	Code	D1	D2	Function
Registers Setting	00100001	X address	Y address	Set cursor pointer
5 5	00100010	Data	00h	Set Offset Register
	00100100	Low address	High address	Set Address pointer
Set Control Word	01000000	Low address	High address	Set Text Home Address
	01000001	Columns	00h	Set Text Area
	01000010	Low address	High address	Set Graphic Home Address
	01000011	Columns	00h	Set Graphic Area
Mode Set	1000X000			OR mode
	1000X001			EXOR mode
	1000X011			AND mode
	1000X100			Text Attribute mode
	10000XXX			Internal CG ROM mode
	10001XXX			External CG RAM mode
Display Mode	10010000			Display off
	1001XX10			Cursor on, blink off
	1001XX11			Cursor on, blink on
	100101XX			Text on, graphic off
	100110XX			Text off, graphic on
	100111XX			Text on, graphic on
Cursor Pattern Select	10100000			1-line cursor
	10100001			2-line cursor
	10100010			3-line cursor
	10100011			4-line cursor
	10100100			5-line cursor
	10100101			6-line cursor
	10100110			7-line cursor
	10100111			8-line cursor
Data Read/Write	11000000	Data		Data Write and Increment ADP
	11000001			Data Read and Increment ADP
	11000010	Data		Data Write and Decrement ADP
	11000011			Data Read and Decrement ADP
	11000100	Data		Data Write and Non-variable ADP
Data auto Read/Write	11000101			Data Read and Non-variable ADP
Data auto Read/write	10110000			Set Data Auto Write
	10110001			Set Data Auto Read Auto Reset
Screen Peek	10110010 11100000			Screen Peek
Screen Copy				Screen Copy
Bit Set/Reset	11101000 11110XXX			Bit Reset
Bit Sel/Reset	11111XXX			Bit Set
	1111X000			Bit 0 (LSB)
	1111X001			Bit 1
	1111X010			Bit 2
	1111X011			Bit 3
	1111X100			Bit 4
	1111X101			Bit 5
	1111X110			Bit 6
	1111X111			Bit 7 (MSB)
Screen Reverse	11010000	Data		Whole screen reverse
				Data Bit 0
				0 : Normal
				1 : Reverse
Blink Time	01010000	Data	Don't care (Note)	If Frame = 60Hz Data Bit 2:0
			(11010)	000 : 0.066s
				001:0.25s
				010 : 0.5s (Default) 011 : 0.75s
				100 : 1s
				101 : 1.25s
				110:1.5s
Cursor Auto Moving	01100000	Data	Don't care	111 : 2s
Cursor Auto Moving	01100000	Data	Don't care (Note)	
			(Note)	<u>111 : 2s</u> Data Bit 0 0 : Disable.(Default) 1 : Enable.
Cursor Auto Moving	01100000	Data Data	(Note) Don't care	111 : 2s Data Bit 0 0 : Disable.(Default) 1 : Enable. Data Bit 1:0
			(Note)	111 : 2s Data Bit 0 0 : Disable.(Default) 1 : Enable. Data Bit 1:0 00 : Do not care.(Default)
			(Note) Don't care	111 : 2s Data Bit 0 0 : Disable.(Default) 1 : Enable. Data Bit 1:0

Note : In these functions, it must be sent two data before sending the command, but the contents c the second datum (D2) can be any values.

7. CHARACTER GENERATOR ROM

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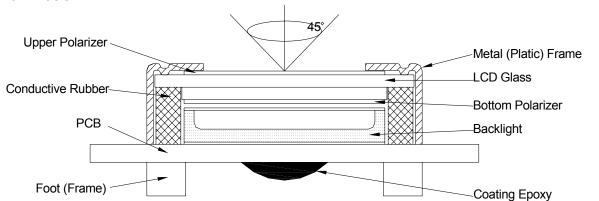
CGR	ROM F	ont -	01													
LSB	0	1	2	3	4	5	6	7	8	9	A	В	С	D	E	F
0																
1							8									
2			B													
3			R													
4												k		T		
5											7	ł				
6																
7												4				
CGF	ROM F	-ont	- 02													
LSE	0	1	2	3	4	5	6	7	8	9	А	в	с	D	E	F
0							8									
1									1							
2											J	ĸ				
3									X							
4												7				
5												1		7		
-					ŀ											
6																HH

The RA6963 has two part number - RA6963L2NA and RA6963L2NB. The RA6963L2NA is compatible to T6963C(code 0101) and the default font is Figure 6-13 as above. The RA6963L2NB is compatible to T6963C(code 0201) and the default font is Figure 6-14 as above.

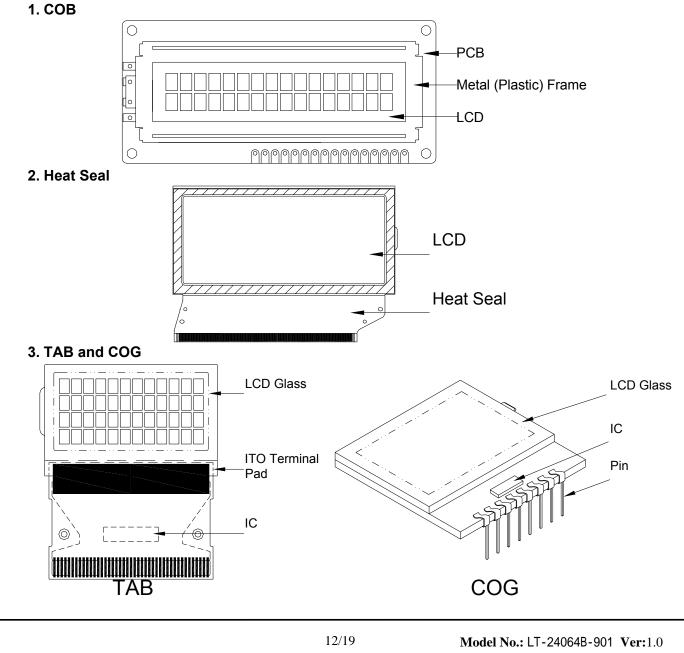
Although RA6963 provide a extra internal command for MCU to select both font of above, but you do not need to change the software to select the font that if you chose the right part number.

8. QUALITY SPECIFICATIONS

- 8 1. LCM Appearance and Electric inspection Condition
 - 1. Inspection will be done by placing LCM 30cm away from inspector's eyeballs under normal illumination.



- 2. View Angle: with in 45° around perpendicular line.
- 8 2. Definition



8-3. Sampling Plan and Acceptance

1.Sampling Plan

MIL - STD - 105E (||) ordinary single inspection is used.

2.Acceptance Major defect:

AQL = 0.25%
AQL = 0.65%

8-4. Criteria

1.COB

Defect	Inspection Item	Inspection Standards		
Major	PCB copper flakes peeling off	Any copper flake in viewing Area should be greater than 1.0mm ²	Reject	
Major	Height of coating epoxy	Exceed the dimension of drawing	Reject	
Major	Void or hole of coating epoxy	Expose bonding wire or IC	Reject	
Major	PCB cutting defect	Exceed the dimension of drawing	Reject	

2.SMT

Defect	Inspection Item	Inspection Standa	ards
Minor	Component marking not readable		Reject
Minor	Component height	Exceed the dimension Of drawing	Reject
Major	Component solder defect (missing , extra, wrong component or wrong orientation		Reject
Minor	Component position shift x component soldering pad x y	X < 3/4Z Y > 1/3D	Reject Reject
Minor	Component tilt component D Soldering pad	Y > 1/3D	Reject
Minor	Insufficient solder component PAD PCB	θ <u><</u> 20°	Reject

Defect	Inspection Item	Ir	nspection Standa	rds	
Major	Crack / breakage	Any	Anywhere		
		W	L	Acceptable of Scratch	
Minor		w<0.1mm	Any	Ignore	
		0.1 <u><</u> w<0.2mm	L <u><</u> 5.0mm	2	
	Frame Scratch	0.2 <u><</u> w<0.3mm	L <u><</u> 3.0mm	1	
		w <u>></u> 0.3mm	Any	0	
			reater than 5mm. on the back sid gnored .	e of frame (ne	
				Acceptable of Dents / Pricks	
	Frame Dent , Prick	Φ <u><</u> 1.0mm		2	
		1.0<Ф <u><</u> 1.5mm		1	
Minor	$\Phi = \frac{L + W}{2}$	1.5mm<Φ		0	
	2	Note · 1 Above	critoria applicable	to a second second a second	
		/ pricks with dist	tance greater than rick on the back s	5mm	
Minor	Frame Deformation	/ pricks with dist 2. Dent / pr visible) can be i	tance greater than rick on the back s	ide of frame (no	

4. Flexible Film Connector (FFC)

Defect	Inspection Item	Inspection Standa	rds
Minor	Tilted soldering	Within the angle +5°	Acceptable
Minor	Uneven solder joint /bump		Reject
		Expose the conductive line	Reject
Minor	Hole $\Phi = \frac{L + W}{2}$	Φ > 1.0mm	Reject
Minor	Position shift $Y \xrightarrow{\psi} \xrightarrow{\varphi^{2} \leftarrow \psi}$ $\gamma \xrightarrow{\varphi^{2} \leftarrow \psi}$	Y > 1/3D	Reject
		X > 1/2Z	Reject

5. Screw

Defect	Inspection Item	Inspection Standards	
Major	Screw missing/loosen		Reject
Minor	Screw oxidation	Any rust	Reject
Minor	Screw deformation	Difficult to accept screw driver	Reject

6. Heatseal TCP FPC

Defect	Inspection Item	Inspection Standards	
Major	Scratch expose conductive layer		Reject
Minor	HS Hole $\Phi = \frac{L + W}{2}$	Φ> 0.5mm	Reject
Major	Adhesion strength	Less than the specification	Reject
Minor	Position shift $Y \xrightarrow{-\psi} -\psi$	Y > 1/3D	Reject
MITO		X > 1/2Z	Reject
Major	Conductive line break		Reject

7. LED Backing Protective Film and Others

Defect	Inspection Item	Inspection Standards				
		Acceptable number of units				
	LED dirty, prick	Φ <u><</u> 0.10mm	Ignore			
		0.10<Φ <u><</u> 0.15mm	2			
Minor		0.15<Φ <u><</u> 0.2mm	1			
		Φ>0.2mm	0			
		The distance between any two spots should be \geq 5mm Any spot/dot/void outside of viewing area is acceptable				
Minor	Protective film tilt	Not fully cover LCD	Reject			
Major	COG coating	Not fully cover ITO circuit	Reject			

8. Electric Inspection

Defect	Inspection Item	Inspection Standards	
Major	Short		Reject
Major	Open		Reject

Defect	Inspect Item			Inspection Standards							
		* Glass Scratch	W	W <u><</u> 0.03		0.03 <w<u><0.05</w<u>		V>0.05			
Minor	Linear Defect	* Polarizer Scratch	L	L<5		<5	L<3			Any	
		* Fiber and Linear material	ACC. NO.	1				Reject			
				L is the length and W is the width of the defe						defec	
Minor	Black Spot and Polarizer Pricked	* Foreign material		Φ <u><</u> 0		0.1<Φ <u><</u> 0	.15 (0.15<Φ <u><</u> 0.	.2	Φ>0.2	
		between glass and		3EA 100m		2		1		0	
		polarizer or glass and glass * Polarizer hole or protuberance by external force	Note	Φ is tl	Φ is the average diameter of the defect. Distance between two defects > 10mm.						
Minor	White Spot and Bubble in polarizer	* Unobvious	Φ Φ <u><</u> 0.3		0.3	0.3<Φ <u><</u> 0.5 0		5<Ф			
		transparant foreign material between	ACC. NO.	3EA / 100mm ²			1		0		
		glass and glass or glass and polarizer * Air protuberance between polarizer and glass	Note	Φ is the average diameter of the defect. Distance between two defects > 10mm.					Γ		
	Segment Defect		Φ	Ф <u><</u> 0.	10	0.10<Φ <u><</u>	0.20	0.20<Φ <u><</u>	<u><</u> 0.25	Φ>0.2	
Minor			ACC. NO.	3EA 100m	./ m²	2		1		0	
		t view		W is more than 1/2 s		segment width		Rejec			
			Note	$\Phi = \frac{L + W}{2}$ Distance between tw			wo de	efect is 10	mm	I	
Minor	Protuberant Segment		Φ	—		0.10<Φ <u><</u> 0.20		—		Φ>0.2	
			w	Glu	Glue W <u><</u> 1/2 S W <u><</u> 0.				Ignor		
		Φ = (L + W) / 2	ACC. NO.	3EA / 100mm²		2		1		0	
Minor	Assembly Mis-alignment		1. Segment								
			E			-				.0mm	
										<0.25	
			Judge Acceptable Acceptable Acceptable 2. Dot Matrix Acceptable Acceptable Acceptable Acceptable								
									Rejec		
Minor	Stain on LCD Panel Surface		Accept when stains can be wiped lightly with a sof cloth or a similar one. Otherwise, judged according to the above items: "Black spot" and "White Spot"								

9. RELIABILITY

NO.	Item	Condition	Criterion				
1	High Temperature Operating	70℃, 96Hrs					
2	Low Temperature Operating	-20℃, 96Hrs	No defect in cosmetic and operational function allowable. Total current Consumption should be below double of initial value.				
3	High Humidity	60℃, 90%RH, 96Hrs					
4	High Temperature Storage	80℃, 96Hrs					
5	Low Temperature Storage	-30℃, 96Hrs					
6		Random wave					
	Vibration	10 ~ 100Hz					
	vibration	Acceleration: 2g					
		2 Hrs per direction(X,Y,Z)					
7		-20℃ to 25℃ to 70℃					
	Thermal Shock	(60Min) (5Min) (60Min)					
		16Cycles					
8	ESD Testing	Contract Discharge Voltage: +1 ~ 5kV and –1 ~ –5kV	There will be discharged ten times at every discharging				
O	ESD resung	Air Discharge Voltage: +1 ~ 8kV and –1 ~ -8kV	voltage cycle. The voltage gap is 1kV.				

Note: 1) Above conditions are suitable for standard products.

2) For restrict products, the test conditions listed as above must be revised.

10. HANDLING PRECAUTION

(1) Mounting Method

The panel of the LCD Module consists of two thin glass plates with polarizers which easily get damaged since the Module is fixed by utilizing fitting holes in the printed circuit board. Extreme care should be taken when handling the LCD Modules.

(2) Caution of LCD handling & cleaning

When cleaning the display surface, use soft cloth with solvent (recommended below) and wipe lightly.

- Isopropyl alcohol

- Ethyl alcohol

- Trichloro trifloro thane

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface. Do not use the following solvent:

- Water

- Ketone

- Aromatics

(3) Caution against static charge

The LCD Module use C-MOS LSI drivers, so we recommend that you connect any unused input terminal to VDD or VSS, do not input any signals before power is turned on. And ground your body, Work/assembly table. And assembly equipment to protect against static electricity.

(4) Packaging

- Modules use LCD elements, and must be treated as such. Avoid intense shock and falls from a height.

- To prevent modules from degradation. Do not operate or store them exposed directly to sunshine or high temperature/humidity.

(5) Caution for operation

- It is indispensable to drive LCD's within the specified voltage limit since the higher voltage than the limit shorten LCD life. An electrochemical reaction due to direct current causes LCD deterioration, Avoid the use of direct current drive.

- Response time will be extremely delayed at lower temperature than the operating temperature range and on the other hand at higher temperature LCD's show dark color in them. However those phenomena do not mean malfunction or out of order with LCD's. Which will come back in the specified operating temperature range.

- If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.

- A slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit.

Usage under the relative condition of 40°C, 50%RH or less is reequired.

(6) Storage

- In the case of storing for a long period of time (for instance.) For years) for the purpose or replacement use, The following ways are recommended.
 - Storage in a polyethylene bag with sealed so as not to enter fresh air outside in it, And with no desiccant.
- Placing in a dark place where neither exposure to direct sunlight nor light is. Keeping temperature in the specified storage temperature range.
- Storing with no touch on polarizer surface by the anything else. (It is recommended to store them as they have been contained in the inner container at the time of delivery)

(7) Safety

- It is recommendable to crash damaged or unnecessary LCD into pieces and wash off liquid crystal by using solvents such as acetone and ethanol Which should be burned up later.

When any liquid crystal leaked out of a damaged glass cell comes in contact with your hands, please wash it off well with soap and water.

